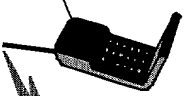


100 ↗

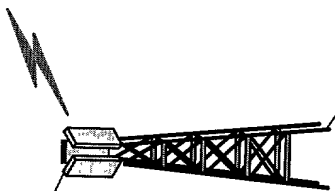
106X



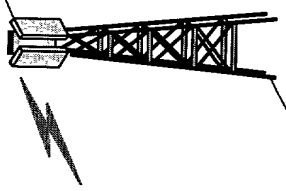
106Y



102A



102B



MOBILE COMMUNICATIONS NETWORK 104

CONTROLLER
UNIT 108

FIG. 1

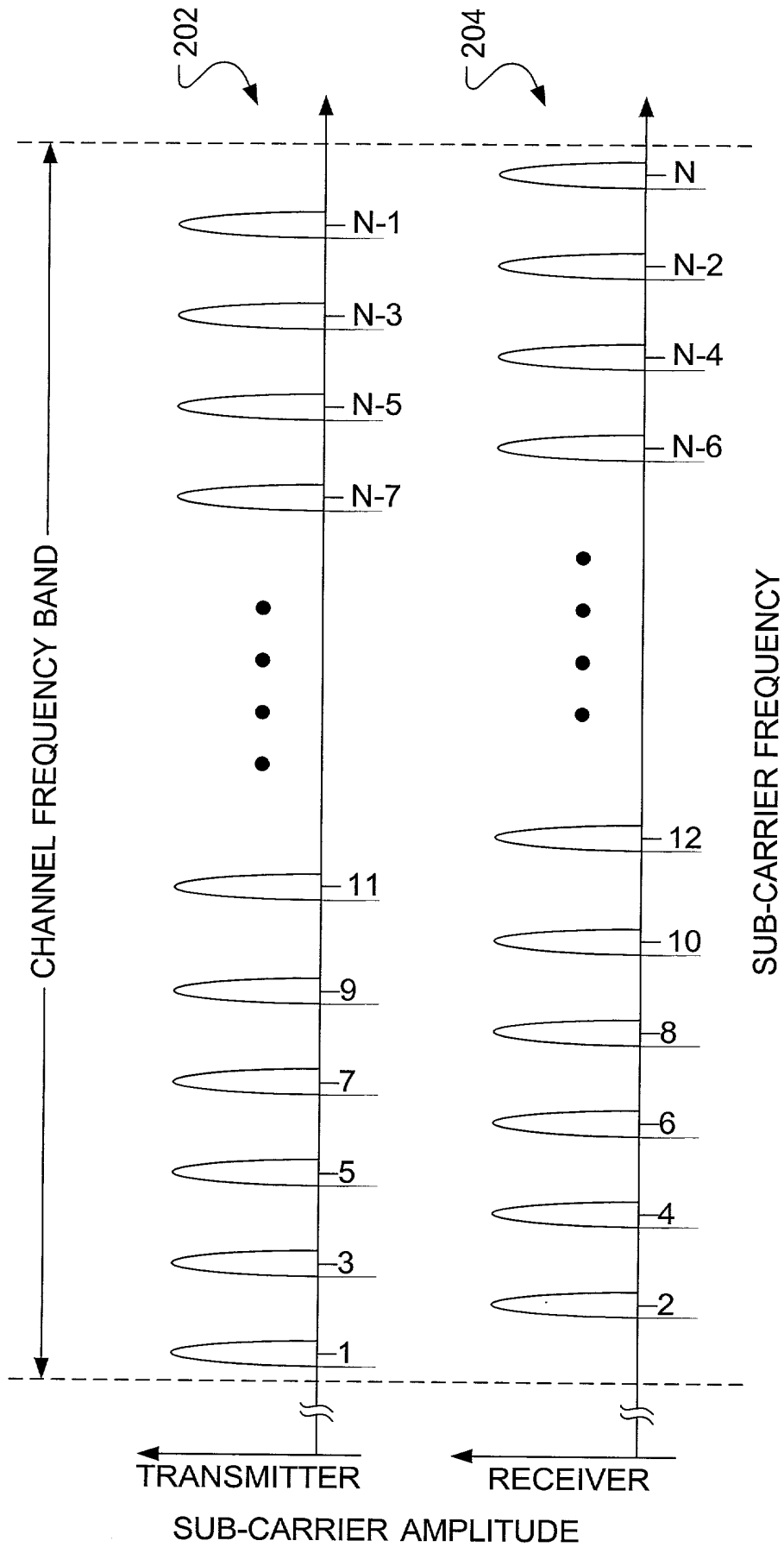


FIG. 2

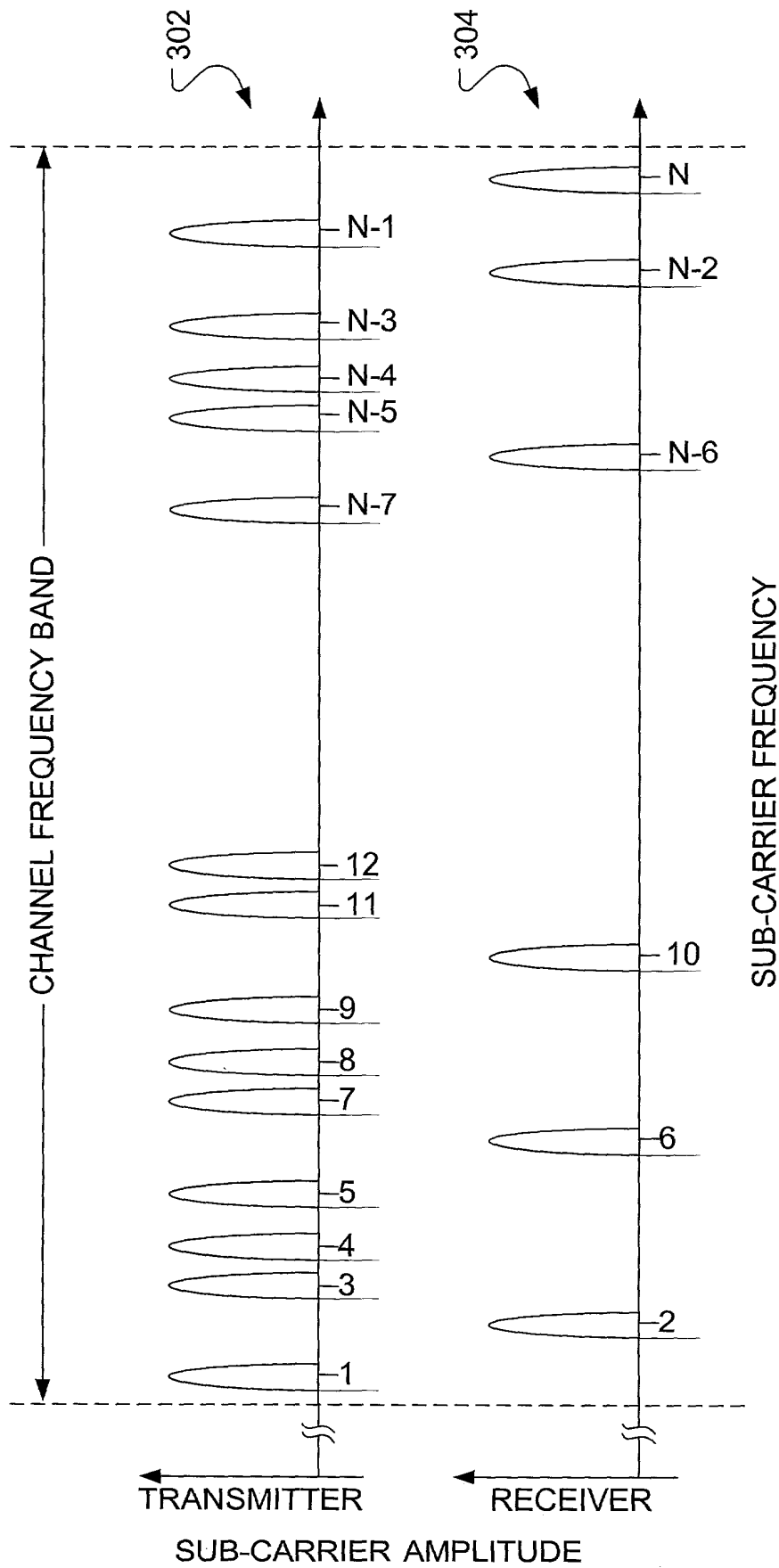


FIG. 3

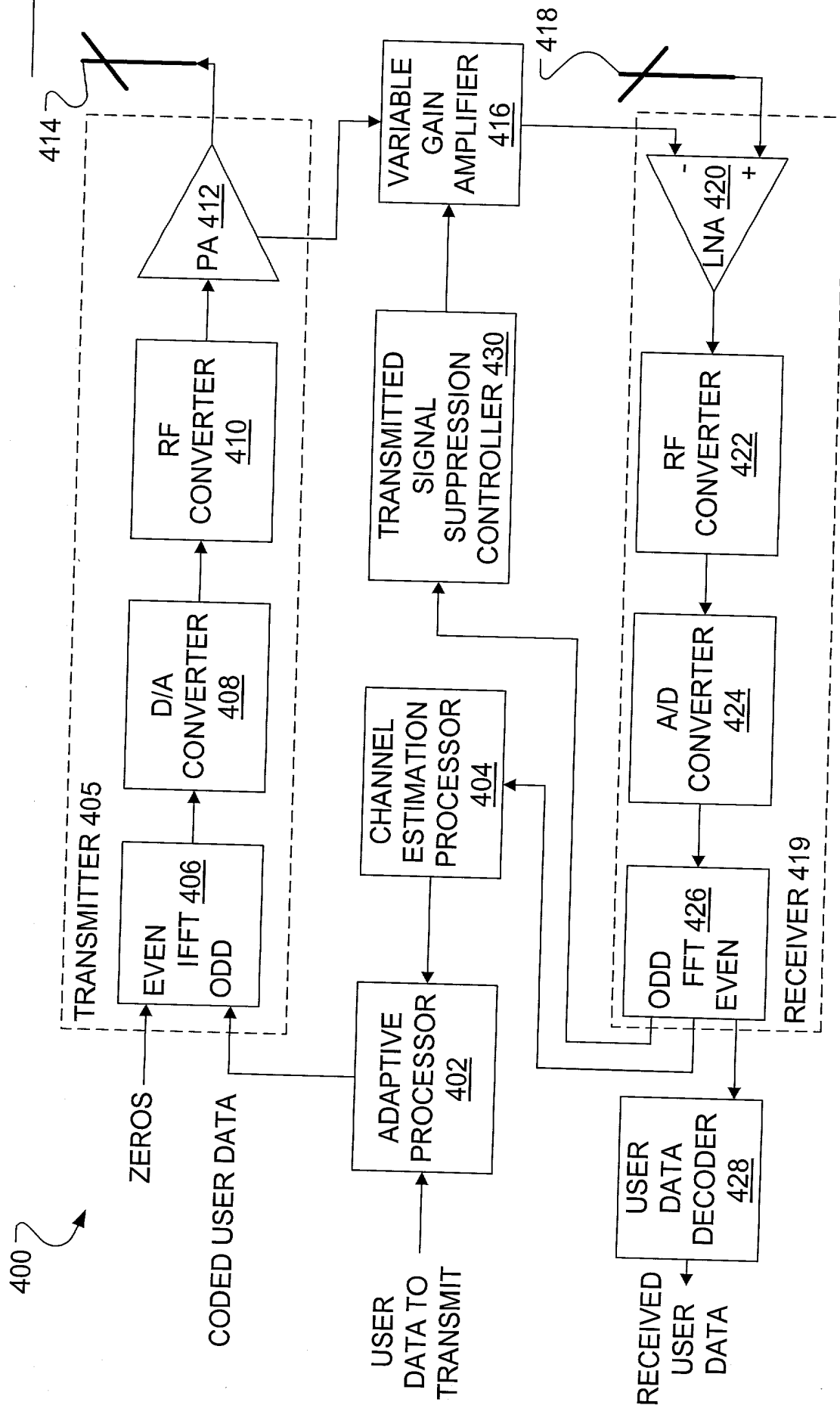
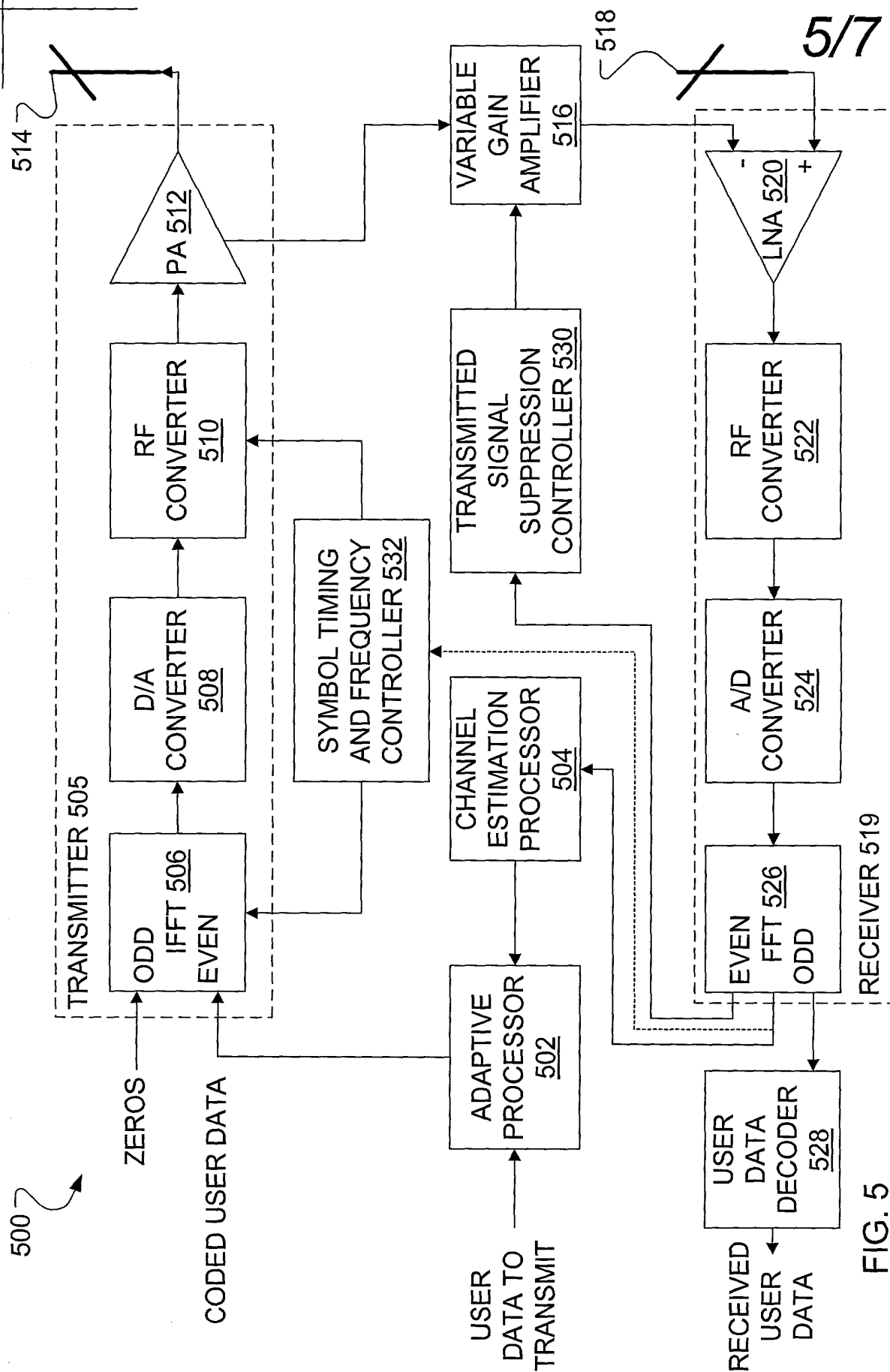
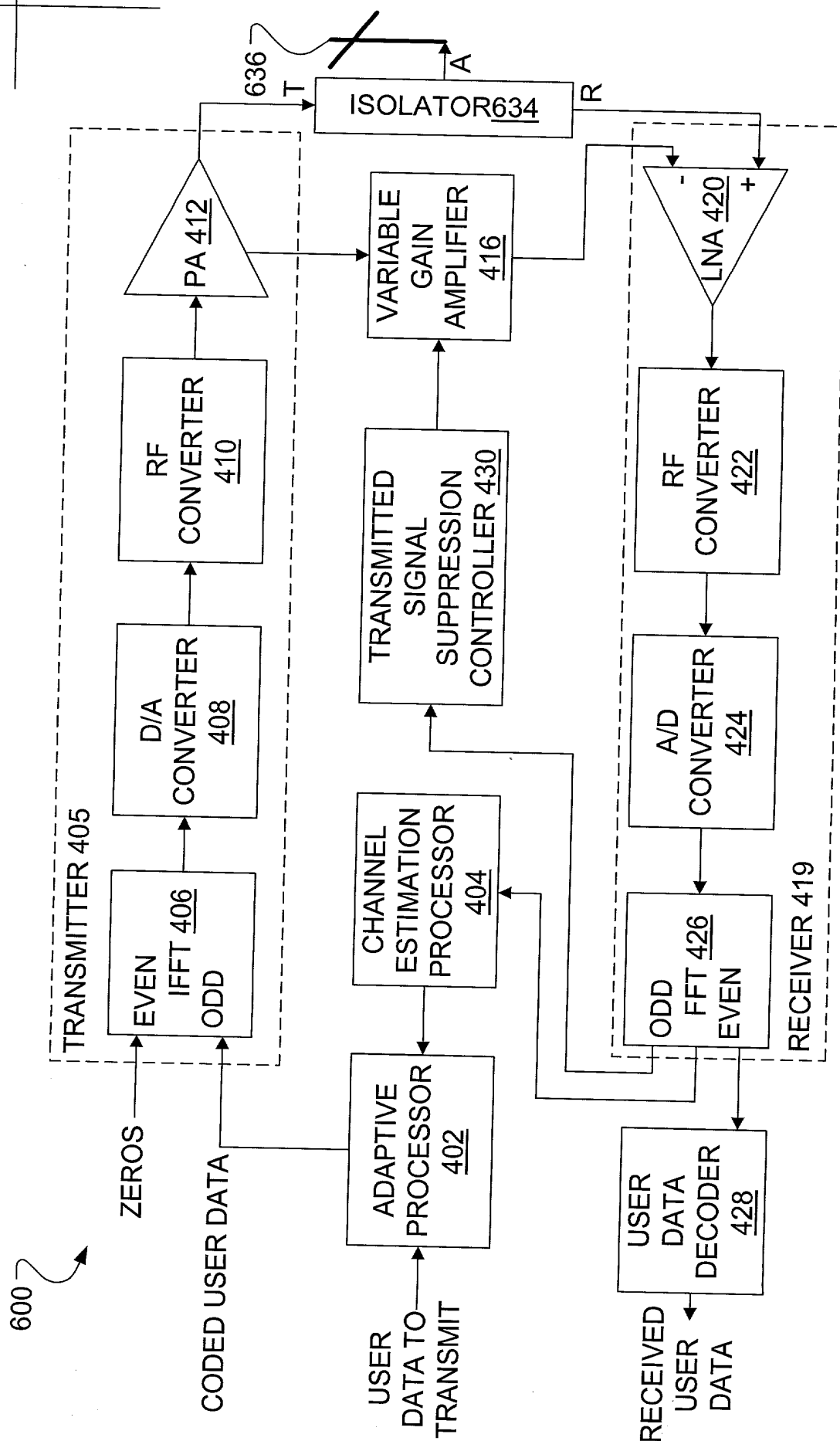


FIG. 4





700

Figure 7 is a block diagram of a communication system 700. The system includes a transmitter 505 and a receiver 519. The transmitter 505 includes an odd IFFT block 506, a D/A converter 508, an RF converter 510, and a power amplifier 512. The receiver 519 includes an LNA 520, an RF converter 522, an A/D converter 524, and an even FFT block 526. A channel estimation processor 504 is connected to both the transmitter and receiver. A transmitted signal suppression controller 530 is connected to the transmitter and receiver. A symbol timing and frequency controller 532 is connected to the transmitter. A variable gain amplifier 516 is connected to the transmitter and receiver. An adaptive processor 502 is connected to the transmitter and receiver. A user data decoder 528 is connected to the receiver. An isolator 734 is connected to the transmitter and receiver. A signal 736 is input to the transmitter and receiver.

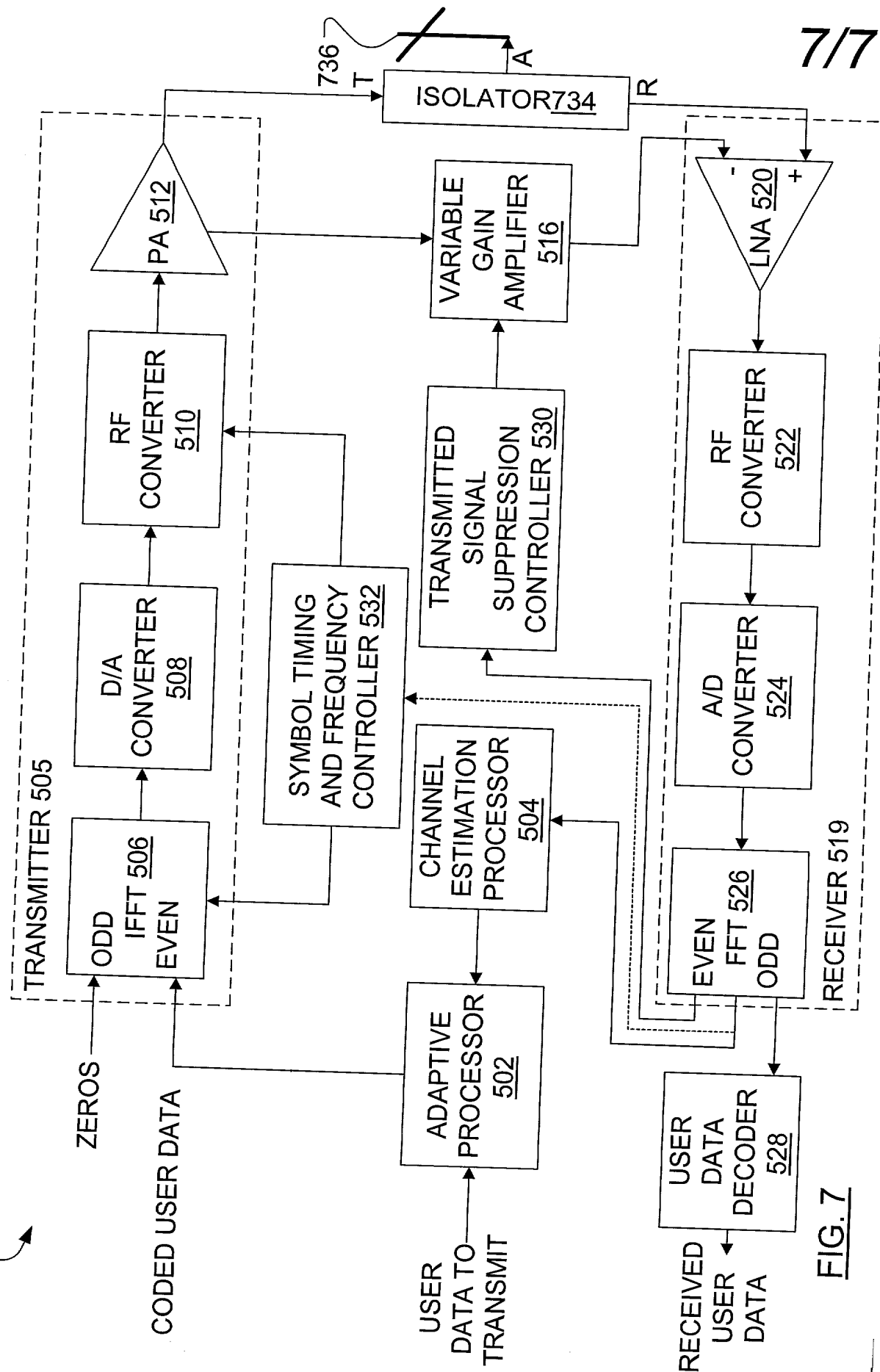


FIG. 7